

OK to enter

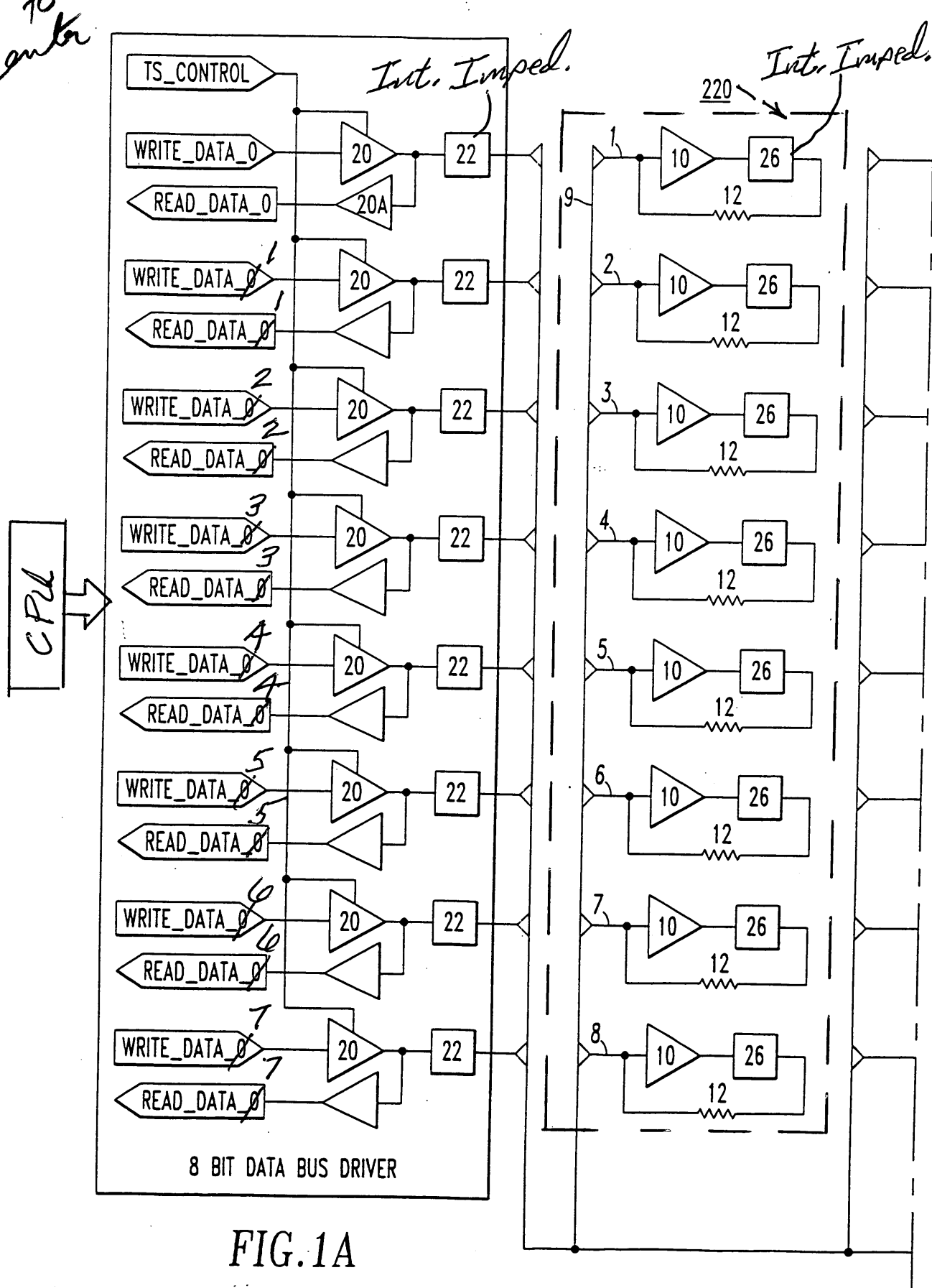
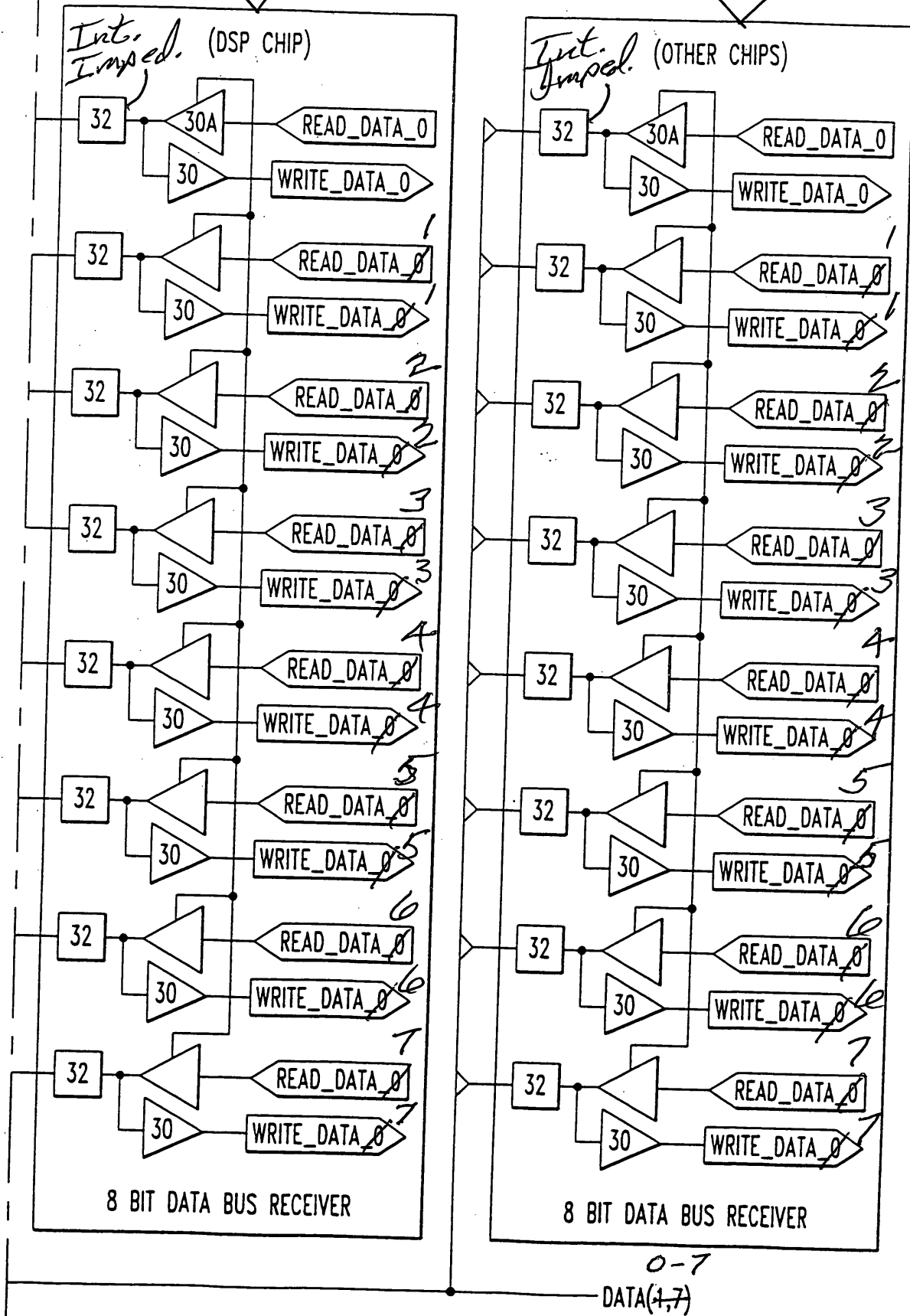


FIG.1A

DSP

CPLD

FIG. 1B



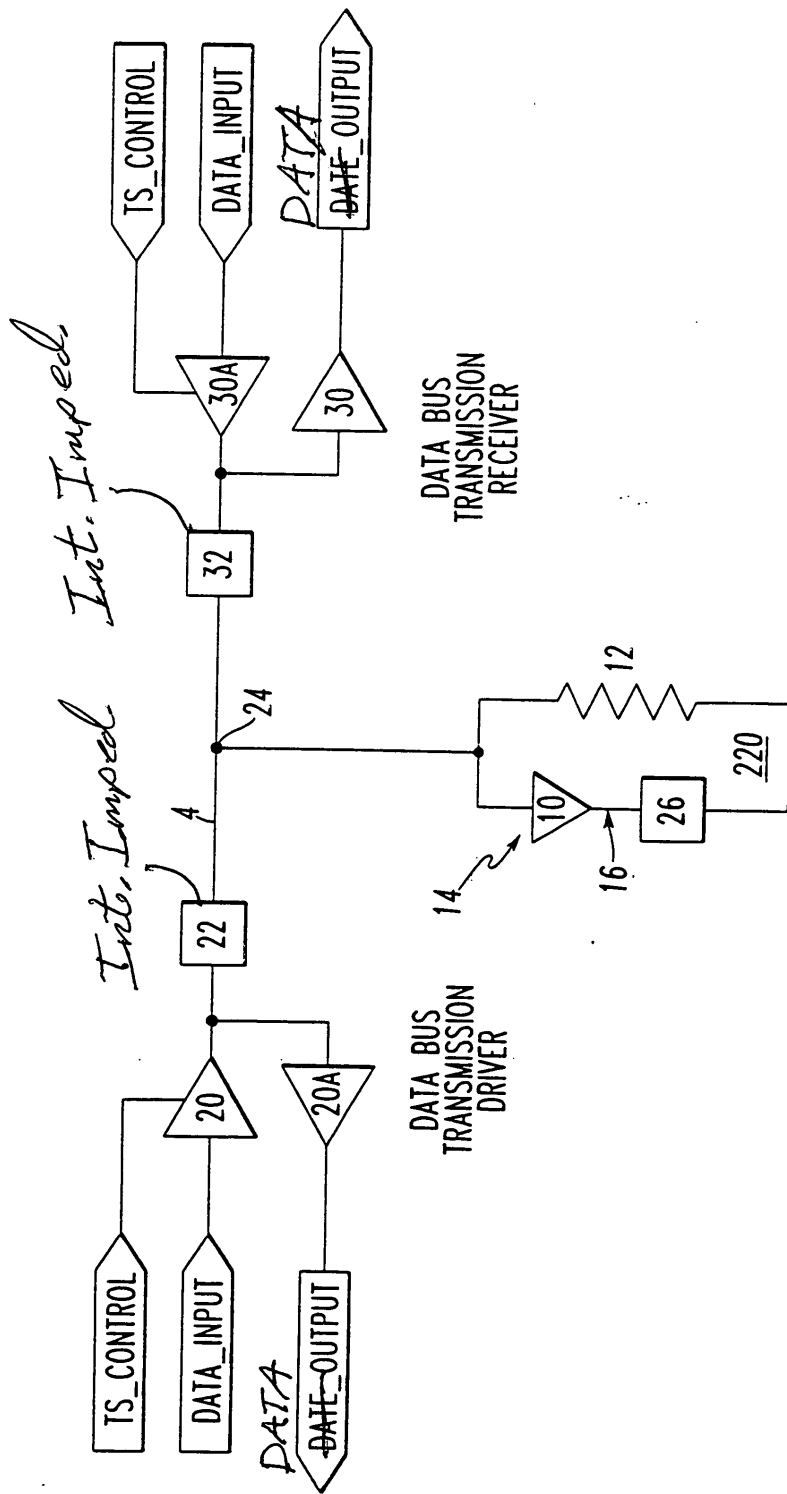


FIG.2